Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the present application.

Listing of Claims:

1. (Currently Amended): A method of programming information in a memory arrangement of a computer, comprising the steps of:

providing an identifier written into an area of the memory arrangement that is to be programmed, wherein the identifier identifying enables identification of a correct programming of the memory arrangement; [[and]]

rendering the identifier unrecognizable by altering the identifier in the memory arrangement before programming the information; and

rendering the identifier recognizable upon correct programming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct programming is concluded, wherein programmed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the programming.

2. (Previously presented): The method according to Claim 1, wherein the computer is a control unit in a motor vehicle.

3. (Previously presented): The method according to Claim 1, wherein the altering step includes the substep of:

altering the identifier by at least one of erasing and programming.

4. (Currently Amended): The method according to Claim 1, further comprising the step of: wherein entering the identifier is located in a section that is programmed last in the area into a further area of the memory arrangement, the further area being programmed only after programming of the area.

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

5.   (Canceled).

6.   (Previously presented): The method according to claim 1, wherein the identifier is a component of the information.

7.   (Currently Amended): The method according to Claim [[1]] 4, ~~further comprising the step of:~~ wherein ~~altering~~ the identifier is altered by at least one of erasing and programming so that the identifier is unidentifiable.

8.   (Previously presented): The method according to claim 1, wherein the identifier is a section of a program identifier which identifies the respective information.

9.   (Previously presented): The method according to Claim 1, further comprising the step of:
        checking the identifier after at least one of (a) an interruption in programming and (b) programming the memory arrangement.

10.   (Previously presented): The method according to Claim 9, further comprising the step of:
        storing the interruption with a flag in the memory arrangement.

11.   (Previously presented): The method according to Claim 10, further comprising the steps of:
        checking at least one of the identifier and the flag before programming; and
        analyzing at least one of the identifier and the flag before programming.

12.   (Currently Amended): A method of reprogramming information in a memory arrangement of a computer, comprising the step of:
        selecting an identifier from the information entered into an area of the memory to be ~~programmed~~ reprogrammed, wherein the identifier ~~identifying~~ enables identification of a correct ~~programming~~ reprogramming of the memory arrangement;
        rendering the identifier unrecognizable by altering the identifier in the memory

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

arrangement before reprogramming the information; and

rendering the identifier recognizable upon correct reprogramming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct reprogramming is concluded, wherein reprogrammed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the reprogramming.

13.   (Previously presented): The method according to Claim 12, wherein the computer is a control unit in a motor vehicle.

14.   (Previously presented): The method according to claim 12, ~~further comprising the step of:~~ wherein ~~selecting~~ the identifier is located in a section that is reprogrammed last in the area ~~from the information entered into a further area~~ of the memory arrangement, ~~the further area~~ being programmed ~~only after programming of the area~~.

15.   (Canceled).

16.   (Canceled).

17.   (Currently Amended): The method according to Claim [[16]] 14, wherein the altering step includes the substep of:

altering the selected identifier by at least one of erasing and programming.

18.   (Currently Amended): The method according to claim 12, , ~~further comprising the step of:~~ wherein ~~selecting~~ the identifier ~~as~~ is at least one section of a predetermined length of the information entered into the memory arrangement.

19.   (Currently Amended): The method according to Claim 12, ~~further comprising the step of:~~ wherein ~~altering~~ the identifier is altered by at least one of erasing and programming so that the identifier is unidentifiable.

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

20. (Previously presented): The method according to claim 12, wherein the identifier is a section of a program identifier which identifies the information.

21. (Currently Amended): The method according to Claim 12, further comprising the step of:

checking the identifier after at least one of (a) an interruption in ~~programming~~ reprogramming and (b) ~~programming~~ reprogramming the memory arrangement.

22. (Previously presented): The method according to Claim 21, further comprising the step of:

storing the interruption with a flag in the memory arrangement.

23. (Currently Amended): The method according to Claim 22, further comprising the steps of:

checking at least one of the identifier and the flag before ~~programming~~ reprogramming; and

analyzing at least one of the identifier and the flag before ~~programming~~ reprogramming.

24. (Currently Amended): A device for programming information in a memory arrangement of a computer, comprising:

a programming arrangement entering an identifier into an area of the memory arrangement to be programmed, wherein the identifier ~~identifying~~ enables identification of a correct programming of the memory arrangement, the programming arrangement rendering the identifier unrecognizable by altering the identifier in the memory arrangement before programming the information, and the programming arrangement rendering the identifier recognizable upon correct programming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct programming is concluded, wherein programmed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the programming.

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

25. (Previously presented): The device according to Claim 24, wherein the computer is a control unit in a motor vehicle.

26. (Previously presented): The device according to Claim 24, wherein the identifier is altered by at least one of erasing and programming.

27. (Currently Amended): A device, comprising:

a reprogramming arrangement reprogramming information in a memory arrangement of a computer, the reprogramming arrangement selecting an identifier from the information entered into an area of the memory arrangement to be ~~programmed~~ reprogrammed, wherein the identifier ~~identifying~~ enables identification of a correct ~~programming~~ reprogramming of the memory arrangement, the reprogramming arrangement rendering the identifier unrecognizable by altering the identifier in the memory arrangement before reprogramming the information, and the reprogramming arrangement rendering the identifier recognizable upon correct reprogramming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct reprogramming is concluded, wherein reprogrammed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the reprogramming.

28. (Previously presented): The device according to Claim 27, wherein the computer is a control unit in a motor vehicle.

29. (Previously presented): The method of claim 1 wherein the information includes data.

30. (Previously presented): The method of claim 1 wherein the information includes programs.

31. (Previously presented): The method of claim 12 wherein the information includes data.

32. (Previously presented): The method of claim 12 wherein the information includes programs.

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

33.　(Currently Amended): A method of erasing information in a memory arrangement of a computer, comprising:

　　providing an identifier written into an area of the memory arrangement that is to be at least partially erased, wherein the identifier identifying enables identification of a correct erasing of the memory arrangement; [[and]]

　　rendering the identifier unrecognizable by altering the identifier in the memory arrangement before erasing the information; and

　　rendering the identifier recognizable upon correct erasing of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing is concluded, wherein remaining information in the memory arrangement is executed only if the identifier is recognized as being correct after the erasing.

34.　(Currently Amended): A device for erasing information in a memory arrangement of a computer, comprising:

　　a programming arrangement entering an identifier into an area of the memory arrangement to be at least partially erased, wherein the identifier identifying enables identification of a correct erasing of the memory arrangement, the programming arrangement rendering the identifier unrecognizable by altering the identifier in the memory arrangement before erasing the information, and the programming arrangement rendering the identifier recognizable upon correct erasing of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing is concluded, wherein remaining information in the memory arrangement is executed only if the identifier is recognized as being correct after the erasing.

35.　(Currently Amended): A method of erasing and programming information in a memory arrangement of a computer, comprising:

　　providing an identifier written into an area of the memory arrangement that is to be erased and programmed, wherein the identifier identifying enables identification of a correct erasing and programming of the memory arrangement; [[and]]

　　rendering the identifier unrecognizable by altering the identifier in the memory

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

arrangement before erasing and programming the information; and

rendering the identifier recognizable upon correct erasing and programming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing and programming is concluded, wherein programmed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the erasing and programming.

36. (Currently Amended): A method of reprogramming information in a memory arrangement of a computer, comprising:

selecting an identifier from the information entered into an area of the memory to be reprogrammed, wherein the identifier ~~identifying~~ enables identification of a correct erasing and ~~programming~~ reprogramming of the memory arrangement;

rendering the identifier unrecognizable by altering the identifier in the memory arrangement before erasing and reprogramming of the area of the memory arrangement; and

rendering the identifier recognizable upon correct erasing and reprogramming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing and reprogramming is concluded, wherein reprogrammed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the erasing and reprogramming.

37. (Currently Amended): A device for erasing and programming information in a memory arrangement of a computer, comprising:

a programming arrangement entering an identifier into an area of the memory arrangement be erased and programmed, wherein the identifier ~~identifying~~ enables identification of a correct erasing and programming of the memory arrangement, the programming arrangement rendering the identifier unrecognizable by altering the identifier in the memory arrangement before erasing and programming the information, and the programming arrangement rendering the identifier recognizable upon correct erasing and programming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing and programming is concluded, wherein programmed information in the area of the memory arrangement is executed only if the identifier is

Appl. No. 09/527,424
Reply to Office Action of May 5, 2005

[10191/1333]

recognized as being correct after the erasing and programming.

38.   (Currently Amended): A device for reprogramming information, comprising:

an arrangement for reprogramming information in a memory arrangement of a computer, the reprogramming arrangement selecting an identifier from the information entered into an area of the memory arrangement to be erased and ~~programmed~~ reprogrammed, wherein the identifier ~~identifying~~ enables identification of a correct erasing and ~~programming~~ reprogramming of the memory arrangement, the reprogramming arrangement rendering the identifier unrecognizable by altering the identifier in the memory arrangement before erasing and reprogramming the memory arrangement, and the reprogramming arrangement rendering the identifier recognizable upon correct erasing and reprogramming of the area of the memory arrangement, whereby the identifier is able to be detected only after the correct erasing and reprogramming is concluded, wherein reprogrammed information in the area of the memory arrangement is executed only if the identifier is recognized as being correct after the erasing and reprogramming.